	Application No.	Applicant(s)	
Notice of Allowability	09/918,792 Examiner	CASTILLEJO ET AL. Art Unit	
	Examiner	Artonit	
	Benny Q. Tieu	2642	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS Is herewith (or previously mailed), a Notice of Allowance (PTOL-88 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT I of the Office or upon petition by the applicant. See 37 CFR 1.3	S (OR REMAINS) CLOSED 5) or other appropriate comm RIGHTS. This application is	n this application. If not included unication will be mailed in due course.	
1. \boxtimes This communication is responsive to <u>the response to Ele</u>	ction/ Restriction filed April 1	2 <u>, 2005</u> .	
2. The allowed claim(s) is/are <u>1-9</u> .	, ,		
3. The drawings filed on 30 July 2001 are accepted by the E	Examiner.		
 4. Acknowledgment is made of a claim for foreign priority of a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 	ve been received.		
3. Copies of the certified copies of the priority d	ocuments have been receive	ed in this national stage application from	ı the
International Bureau (PCT Rule 17.2(a)).		•	
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the requiremen	nts
5. A SUBSTITUTE OATH OR DECLARATION must be sub- INFORMAL PATENT APPLICATION (PTO-152) which gi)F
6. CORRECTED DRAWINGS (as "replacement sheets") mi	ust be submitted.		
(a) including changes required by the Notice of Draftspe	rson's Patent Drawing Revie	w (PTO-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date	_•		
(b) including changes required by the attached Examine Paper No./Mail Date	r's Amendment / Comment o	r in the Office action of	
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in	1.84(c)) should be written on the header according to 37 C	the drawings in the front (not the back) of	;
 DEPOSIT OF and/or INFORMATION about the dep attached Examiner's comment regarding REQUIREMENT 	osit of BIOLOGICAL MAT	ERIAL must be submitted. Note the	
Attachment(s) 1. ⊠ Notice of References Cited (PTO-892)		nformal Patent Application (PTO-152)	
 Notice of Draftperson's Patent Drawing Review (PTO-948)) 6. ∐ Interview S Paner No	Summary (PTO-413), /Mail Date	
 Information Disclosure Statements (PTO-1449 or PTO/SB Paper No./Mail Date 3/19/02 	/08), 7. ⊠ Examiner's	Amendment/Comment	
 Examiner's Comment Regarding Requirement for Deposit 	8. 🛛 Examiner's	Statement of Reasons for Allowance	
of Biological Material	9. 🗌 Other	<u>_</u> .	

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. The application has been amended as follows:

Claims 10-14 have been canceled.

(End of Amendment)

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Election/Restrictions

3. Applicant's election without traverse of invention I in the reply filed on April 12, 2005 is acknowledged.

4. This application is in condition for allowance except for the presence of claims 10-14 to an invention non-elected without traverse. Accordingly, claims 10-14 have been cancelled.

Reasons For Allowance

5. The following is an examiner's statement of reasons for allowance: Uemura et al. (U.S. Patent No. 5,874,883) teach a planar-type inductor that can restrain the maximum operable frequency from lowering even if the inductance part of the inductor is miniaturized. This inductor includes an insulating substructure and a patterned conductor layer having a predesigned geometric shape and formed on a chief surface of the substructure. The patterned conductor layer contains an inductance part for providing a wanted inductance value and a first lead part and a second lead part for electrically connecting the inductance part to an external circuit located outside the inductor. The inductance part has a first region and a second region located adjacent to each other at an interval. The inductor further includes a recess formed in the substructure between the first region and the second region of the inductance part. The recess extends along the first region and the second region. A parasitic capacitance between the first region and the second region is reduced due to the existence of the recess. Another patterned conductor layer extending along the pattern of the inductance part of the may be formed, instead of the recess, which reduces the parasitic capacitance due to the electrostatic shielding

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phenomenon (column 3, line 15 to column 4, line 23). However, prior art of record fails to teach, or renders obvious, alone or in combination, an integrated circuit including an inductance structure on a semiconductor substrate comprises a first inductance and second inductance formed by a conductive track having first, second ends and intermediate point that connect to first, second, and third terminals and means for setting the third terminal to high impedance when the first inductance is used as directly claimed in independent claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Benny Q. Tieu whose telephone number is 571-272-7490. The examiner can normally be reached on Monday-Friday: 6:30AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ahmad Matar can be reached on 571-272-7488. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Benny Q. Tleu Primary Examiner

Serry Q. Ten

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